Iterative Decoders with Deliberate Message Flips Approaching Maximum Likelihood Decoding

Bane Vasic¹, Predrag Ivanis², David Declercq³ Khoa LeTrung³ and Elza Dupraz⁴

¹University of Arizona, Tucson and ²University of Belgrade ³ETIS Lab, ENSEA, Université de Cergy-Pontoise-CNRS ⁴Telecom, Bretagne

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Outline



- System Model
- Philosophy
- Main Ingredients

Annihilation of Trapping Sets

- Trapping Sets of Noisy Decoders
- Good Deeds of Logic Gate Errors
- Comparison of Different Schedules

Analysis

- Markov Chain Representation
- Convergence to Absorbing States
- Frame and Miscorrection Error Rates

Discussion

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Memory Architecture

- Each *k*-bit user information is stored as a codeword of an (n, k) linear block code of length *n* and code rate R = k/n.
- The memory elements are unreliable and fail transiently and independently of each other – the von Neumann failure model, BSC(α_M)
- The correction circuit is also unreliable.



"Nothing in this presentation is novel. The only novelty is random errors. Even this joke is not novel." Anonymous

> "Is noise always bad?" A Kirkland & Ellis lawyer

Shashi's Theorem (Vasić and Chilappagari, 2007)

Let *G* be a $(\gamma, \rho, \alpha, (3/4 + \epsilon))$ expander for any $\epsilon > 0$. Our memory architecture can tolerate constant fraction of errors in all the components if $\alpha_M + \gamma(\rho - 2)\alpha_{\oplus} + \alpha_{\gamma} < \alpha(1 + 4\epsilon)(4\epsilon)/2$

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- Probabilistic behavior of an iterative decoder due to random deliberate errors in its logic gates can be exploited to our advantage and lead to an improved performance and reduced hardware redundancy.
- Iterative decoding can be viewed as a recursive procedure for Bethe free energy function minimization, and the randomness in a message update may help the decoder to escape from local minima.
- The decoder operates in stochastic gradient descent fashion, but the random perturbations do not require any additional hardware as they are built-in the faulty hardware itself. Thus the decoder harvests good deeds of logic gate errors.



One nail drives out another

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One error drives out another

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- Low-density parity check (LDPC) code and an iterative decoder
 - (γ, ρ) -regular LDPC codes
 - Tanner graph $G = (V \cup C, E), |V| = n |C| = n\gamma/\rho$ and $|E| = n\gamma$
- Noisy Gallager-B message updates
- Rewinding decoder, $\mathcal{F}^{\circ'}(L_R)$
 - If a codeword is not found after L_R iterations, $L_R \ll L$, the decoding algorithm is re-initialized with the word received from the channel.
 - Instead of running the whole *L* iterations, the decoder instead runs $r = L/L_B$ very short rounds.
- Critical gates must be perfect.

An example of a code graph G of an (3, 5)-regular LDPC code



Noisy Gallager-B



$$\nu_{\mathbf{v} \to \mathbf{c}}^{(\ell)} = \Phi(\mathbf{y}_{\mathbf{v}}, \mathbf{m}^{(\ell)}) + \mathbf{e}_{MAJ}^{(\ell)}$$
$$\mu_{\mathbf{c} \to \mathbf{v}}^{(\ell)} = \Psi(\mathbf{n}^{(\ell-1)}) + \mathbf{e}_{\oplus}^{(\ell)}$$

- In this talk: Φ and Ψ require only two types of logic gates: (γ – 1)-input majority logic (MAJ) gates and (ρ – 1)-input XOR gates.
- $e_{MAJ}^{(\ell)}$ and $e_{\oplus}^{(\ell)}$ are independent Bernoulli random variables with parameters α_{MAJ} and α_{\oplus} .

- Instead of running the whole *L* iterations, the decoder instead runs $r = L/L_R$ very short rounds.

$$\mathcal{F}^{\circ'}(L_R) = \underbrace{\mathcal{F}(L_R) \Diamond \mathcal{F}(L_R) \Diamond \cdots \Diamond \mathcal{F}(L_R)}_r.$$

- Note that the plain noisy decoder with no rewinding, $\mathcal{F}(L) = \mathcal{F}^{O^1}(L).$
- To allow the decoder to benefit from errors, large number of iterations is needed under some conditions of gate unreliability.
- However, too many logic gate errors can overwhelm the decoder, and lead to miscorrection.
- Rewinding is a key feature which prevents the accumulation of errors in the messages.
- Applicable to other decoders such as Probabilistic Gradient Descent Bit Flipping (not in this talk).

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(5,3) Trapping Sets in the Tanner (155,64,20) Code







Conditional FER of the Tanner (155,64) code



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Conditional FER of the Tanner (155,64) code



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- FER is dominated by the most critical three-bit error patterns.
- The particular low-weight error pattern cannot be decoded by perfect hardware, but it can be decoded with non-zero probability for a wide range of gate error probabilities α_{MAJ} and α_{\oplus} .
- After sufficient number of iterations, the minimum FER is not achieved for perfect gates but for some nonzero value of the gate error rates α_{MAJ} and α_{\oplus} .
- For a broad range of gate error rates, our decoder actually benefits from logic gate errors.

Average FER



• Both XOR and MAJ gates have the same failure rate $\alpha_M = 2 \times 10^{-3}$



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The Impact of the Number of Iterations L

- Increasing the maximum number of iterations, *L*, reduces the probability that the error pattern remains uncorrected.
- The impact of L is especially noticeable for high reliability gates
 - Hardware errors cannot help much in annihilating trapping sets because the state transition probabilities in \mathcal{W} are small for most transitions other than those that already exist in the perfect decoder.
 - Consequently, the convergence to the attractor of the codeword takes longer (the average convergence time also grows with *n*).
- On the other hand, increasing Z is has stronger effect when gates are very noisy.
- Prolonging the second stage of the decoding algorithm greatly improves the performance of a decoder made of the less reliable hardware.

Plain Decoder \mathcal{F}



- For all *L*, the decoder \mathcal{F} outperforms the ideal decoder $\overline{\mathcal{F}}$.
- For large L, \mathcal{F} approaches the nine-error correction decoder.

Rewinding Decoder $\mathcal{F}^{\circlearrowright}$



- For $\alpha_G = 10^{-2}$, the rewind decoder $\mathcal{F}^{\circlearrowright}$ performs beyond the $\lfloor \frac{d_{\min}-1}{2} \rfloor$ bound.
- The total number of iterations $L = r \times L_R$ is kept the same as for \mathcal{F} .

Lessons Learned for Two Gate Error Rate Regimes

- For <u>more reliable</u> logic gates, large *L* is needed before the decoder start benefiting from the positive effects of logic gate errors.
 - Strategy: The perfect syndrome checker is used in the first twenty iterations, as the average *FER* rapidly decreases only at the beginning of the decoding.
 - Both the final bit-estimation circuit as well as the syndrome checker are turned-off and sufficient number of iterations is allowed before they are turned on again.
 - Clearly, this strategy results in energy saving as the perfect gates are used in a reduced number of iterations.
- For <u>less reliable</u> gates, errors correctable by the perfect decoder may turn uncorrectable, or lead to miscorrections as they may lead to large deviations from the trajectory of the perfect decoder.
 - A solution for this case is to rewind the decoder.
 - The higher the gate error rate, the lower optimal rewind period L_R

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• We consider $\mathcal{F}(L) = \mathcal{F}^{\odot^1}(L_R = L)$ – no rewinding.

$$\boldsymbol{\mu}^{(\ell)} = \Upsilon_{C}(\boldsymbol{\mu}^{(\ell-1)}) + \mathbf{e}_{\boldsymbol{\mu}}^{(\ell)}$$

• $\mu^{(\ell)} = (\mu_c^{(\ell)})_{c \in C}$ – the outgoing messages from <u>check nodes</u>.

- Υ_C is the composition of Φ and Ψ, and define the dynamical system of the perfect decoder, *F*.
- e^(ℓ)_μ (of length = mρ) are the realization of errors at time ℓ that affect computation of messages μ.
- Since Φ is the function of the memory output y, Υ_C also depends on y, thus the transition probabilities depend on the the channel error vector e, and for a given decoder we have an ensemble of Markov chains {W_e}_{e∈{0,1}ⁿ}.

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- For a given a decoder *F* and error pattern **e** in the memory elements, consider the Markov chain *W*_e, with the state space *S* = {0,1}^{|C|ρ}, and the transition probability matrix *P* = (*p*_{ε,δ})_{ε,δ∈S}.
 The transition probabilities between states
 - $p_{\varepsilon,\delta} = \Pr\{\mu^{(\ell)} = \delta | \mu^{(\ell-1)} = \varepsilon\}, \text{ depend on } \alpha_{\oplus} \text{ and } \alpha_{MAJ}.$
- Due to independence of logic gate errors, their effect can be combined into a single conditional probability α_G .

$$p_{arepsilon,\delta} = p_{arepsilon,ar{\delta}} lpha_G^{d_{ar{\delta},\delta}} (1-lpha_G)^{|C|
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- Let δ

 ⊆ Υ_C(ε) be the state of the perfect decoder F

 the state ε.

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Approximation for low logic gate error rates

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$$S = S_{\mathbf{X}} \cup S_{\sim \mathbf{X}} \cup S_{\sim \mathcal{C}},$$

- *S*_x subset of *S* for which all parity check are satisfied, and the variable node decisions form the codeword **x**
- S_{∼x} set of states for which all parity check are satisfied, and the variable node decisions form a codeword different from x.
- S_{~C} set of states for which the variable node decisions are not codewords.

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Closeness to a Codeword



- When the perfect syndrome checker is turned on, and if the Markov chain is the state β ∈ S_x ∪ S_{~x}, the decoding is terminated, and the Markov chain stays in β.
- Thus, the states in S_x and $S_{\sim x}$ are absorbing.



All the states in S_x from W_e are lumped into a single state in M_e . Similarly, the states $S_{\sim x}$ are are also lumped into a single state

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Note that P_{~C,x}, P_{~C,~x} and P_{~C,~C} do not depend on ℓ due to homogeneity.

$$P = \begin{pmatrix} 1 & 0 & \mathbf{0} \\ 0 & 1 & \mathbf{0} \\ P_{\sim \mathcal{C}, \mathbf{x}} & P_{\sim \mathcal{C}, \sim \mathbf{x}} & P_{\sim \mathcal{C}, \sim \mathcal{C}} \end{pmatrix} = \begin{pmatrix} I_2 & \mathbf{0} \\ R & Q \end{pmatrix}$$

- The transition diagram of the transient states is a strongly connected graph
- *Q* does not have any nonzero entries
- The transition probabilities from transient to absorbing states S_x and S_{~x} are given by the matrix R = (R_x, R_{~x})

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 $FER_{e}^{(\infty)} = MER_{e}^{(\infty)}$

Theorem

For the noisy Gallager-B decoding algorithm D = F(L, L) on any LDPC code C, and sufficiently large L

 $\textit{FER}_{e}^{(\textit{L})}(\mathcal{D}) \approx \textit{MER}_{e}^{(\textit{L})}(\mathcal{D}).$

Illustration on a (5,1,5) Repetition Code



Conditional FER for error pattern **e**=(11000) as a function of Z $\alpha_{\oplus MAJ} = 0.01$, $L_R = 25$

Outline

- Problem Statement and Philosophy
 - System Model
 - Philosophy
 - Main Ingredients

Annihilation of Trapping Sets

- Trapping Sets of Noisy Decoders
- Good Deeds of Logic Gate Errors
- Comparison of Different Schedules

3 Analysis

- Markov Chain Representation
- Convergence to Absorbing States
- Frame and Miscorrection Error Rates

Discussion

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- For a perfect decoder, α_{MAJ} = 0 and α_⊕ = 0, the transitions between states are deterministic, and the attractor basin of a dynamical system (μ^(ℓ) = Υ_V(μ^(ℓ-1)) includes
 - (i) codewords which are the fixed points
 - ii) trapping sets which can be either fixed points or cycle attractors.
- Perfect decoder may oscillate between these states, thus failing to converge to a codeword.
- On the other hand, in a noisy decoder every state can be reached with a nonzero probability.
- Thus, the faulty decoder will eventually converge to a codeword either correct or incorrect one.
- When the decoding algorithm have small probability of miscorrection in the first decoding iterations, it is better to use the rewinding decoder with $r = L/L_R$ rounds, $L_R \ll L$.

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Thank you!

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B. Vasić and S. K. Chilappagari. An information theoretical framework for analysis and design of nanoscale fault-tolerant memories based on low-density parity-check codes. *IEEE Transactions on Circuits and Systems I: Regular Papers*, 54(11): 2438–2446, Nov. 2007.